

Capital University of Science and Technology, Islamabad Department of Electrical Engineering

Subject: UG Crash Lab Course Execution – (August 10- September 2, 2020)
(BATCH INTAKE-2017, 2018 & 2019)

As per directions of PEC, Department of Electrical Engineering CUST planned to execute the UG labs, focusing the psychomotor part of the learning skills in summer 2020. A proper time table with balanced sectioning of students was prepared and executed to ensure compliance to health and safety measures against COVID19 following the guidelines of HEC, PEC, government and the institute.

The crash program was scheduled from August 10, 2020 (Monday) to September 2, 2020 (Wednesday). During this time period, the labs of batches 193, 183 and 173 were executed successfully. The features of this crash program included:

- 6 laboratory courses of EE with 19 sections created especially in accordance with COVID19 SoPs
- 1 laboratory course of CE (taught by EE instructor) with 4 sections created especially in accordance with COVID19 SoPs
- 2 laboratory courses of ME (taught by EE instructor) with 10 sections created especially in accordance with COVID19 SoPs
- The duration of the each lab section was of 10.5 hrs, distributed in 3 equal time slots.
- The evaluation through rubrics was performed after hands-on experimentation.

The details of the conducted labs are as under:

S.#	Lab Name & Code	Program	Students Average strength		Attendance
			Total	Present	
1	Basic Electrical Engineering Lab (EECE1091)	Civil	95	91	95.79%
2	Electronics Engineering Lab (EEME2751)	Mechanical	88	88	100%
3	Electrical Engineering Lab (EEME2741)	Mechanical	60	58	96.67%
4	Linear Circuit Analysis Lab (EE1211)	Electrical	28	27	96.42%
5	Workshop Practice (EE1021)	Electrical	28	27	96.42%
6	Electronics Circuit Design Lab (EE2231)	Electrical	46	45	96.83%
7	Power Distribution and Utilization Lab (EE3141)	Electrical	62	61	98.39%
8	Instrumentation and measurements Lab (EE3261)	Electrical	54	51	94.45%
9	Applied Mechanics Lab (MEEE2021)	Electrical	7	6	85.71%

Some of the highlights are attached:



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Student following SOP guidelines



Students giving the final exam of Basic Electrical Engineering Lab



Instructor briefing student in Analog Lab II



Students testing circuits in Digital Lab



Students taking readings in Electronics Lab

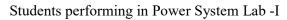


Students performing experiments in Digital Lab



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Students performing in Instrumentation Lab